

CYPR-CD00185

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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

PRE APPLICATION OF:

Warren SNYDER et al.

SERIAL NO: 09/975,030

FILED: OCTOBER 10, 2001

FOR: EMULATOR CHIP/BOARD  
ARCHITECTURE AND  
INTERFACE

:

: GROUP ART UNIT:

:

: EXAMINER:

#3 / Pre Amendment  
J. Gaynor  
12/02

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PRELIMINARY AMENDMENT

Technology Center 2100

COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Prior to examination of the above-identified application, consideration of the following  
Amendments and Remarks is respectfully requested.

IN THE SPECIFICATION

Please replace the paragraph at page 1, lines 11-28 with the following paragraph:

A This application is related to U.S. Patent Application Serial No. 09/975,115, docket number CYPR-CD00182, to Warren Snyder, et al., entitled "IN-SYSTEM CHIP EMULATOR ARCHITECTURE"; and to U.S. Patent Application Serial No. 09/975,104, docket number CYPR-CD00183, to Warren Snyder, entitled "CAPTURING TEST/EMULATION AND ENABLING REAL-TIME DEBUGGING USING AN FPGA FOR IN-CIRCUIT EMULATION"; and to U.S. Patent Application Serial No. 09/975,338, docket number CYPR-CD00186, to Warren Snyder, et al., entitled "METHOD FOR BREAKING EXECUTION OF (TEST) CODE IN A DUT AND EMULATOR CHIP ESSENTIALLY SIMULTANEOUSLY